PTO/SB/08a

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

ATTY. DOCKET NO.	SERIAL NO.
550-486	10/714,561

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APPLICANT

WATT et al

FILING DATE	TC/A.U.
November 17, 2003	2131

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

	Related U.S. Patent Application No. 10/714,518, filed 11/17/03, Watt et al
	Related U.S. Patent Application No. 10/714,563, filed 11/17/03, Watt et al
	Related U.S. Patent Application No. 10/714,178, filed 11/17/03, Orion et al
	Related U.S. Patent Application No. 10/713,456, filed 11/17/03, Orion et al
	Related U.S. Patent Application No. 10/714,521, filed 11/17/03, Belnet et al
	Related U.S. Patent Application No. 10/713,303, filed 11/17/03, Symes
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*Examiner		Date Considered	2/7/2007

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.		SERIAL NO.
	550-486		To be assigned
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	WATT, S. et al.		
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	DOCUMENT	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	NO
✗	02/03208 A2	01/2002	WO				
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✗	97/36235 A1	10/1997	WO				

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✗	Trusted Computing Group (TCB), Main Specification Version 1.1a, 1 September 2001, pp. i-x and 1-322.

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	APPLICANT WATT et al	
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✓	D.N. Jutla et al, "Improving Applications Performance: A Memory Model and Cache Architecture" *Computer Architecture News*, Vol. 25, No. 4, 9/1997, pages 22-29.
✓	L.A. Plana et al, "SPA – A Synthesisable Amulet Core for Smartcard Applications" Proceedings of the Eighth International Symposium on Asynchronous Circuits and Systems, 4/2002, pages 201-210.
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✓	T. Chiueh et al, "Integrating Segmentation and Paging Protection for Safe, Efficient and Transparent Software Extensions" *Operating Systems Review (SIGOPS)* Vol. 33, No. 5, 12/1999, pages 140-153.
✓	M. Levy, "MPF Hosts Premiere of ARM1136. Cores Combine ARMv6, Eight-Stage Pipeline, and More" *Microprocessor Report*, 10/2002, pages 1-4.

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✓	4,521,852	6/1985	Guttag			

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